(SOLE INVENTOR)
IBM Docket No.: FI920030315US1

Declaration and Power of Attorney for Patent Application

As below named inventor, I hereby declare that:

the specification of which (check one)

Application Number

My residence, post office address and citizenship are as stated below next to my name; I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

OVERLAP STACKING OF CENTER BUS BONDED MEMORY CHIPS FOR DOUBLE DENSITY AND METHOD OF MANUFACTURING THE SAME

×	is attached hereto. was filed on	as Application Serial No	and was amended on	
		and understand the contents of nendment referred to above.	of the above-identified specification	n
acknowledge 1.56.	the duty to disclose info	ormation which is material to th	ne patentability as defined in 37 C	FF
patent or invent country other th oreign applicat	tor's certificate, or §365 nan the United States, li	(a) of any PCT International appl sted below and have also identifition's certificate, or PCT Internation	65(b) of any foreign application(s) flication which designated at least of fied below, by checking the box, a ional application having a filing date.	ne ny
Prior Fo Number	oreign Application(s): Country	Day/Month/Year	Priority Claimed	
hereby claim t	he benefit under 35 USC	C §119(e) of any United States pr	rovisional application(s) listed belov	v:

Filing Date

IBM Docket No.: FIS920030315US1

I hereby claim the benefit under 35 USC §120 of any United States application(s), or §365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 USC §112, I acknowledge the duty to disclose information material to the patentability of this application as defined in 37 CFR §1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

Prior	U.S.	Appl	licati	ons:
-------	------	------	--------	------

Serial No.

Filing Date

Status (patented, pending, abandoned)

I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: Anthony P. DeLio (Reg. No. 18,729); Peter W. Peterson (Reg. No. 31,867); John J. Tomaszewski (Reg. No. 26,241); Robert Curcio (Reg. No. 44, 638); Kelly M. Reynolds (47,898); Joseph P. Abate (Reg. No. 30,238); Jay H. Anderson (Reg. No. 38,371); Mark Bilak, 47423: Ira D. Blecker (Reg. No. 29,894); Steven Capella (Reg. No. 33,086); James J. Cioffi (Reg. No. 51,564); Harold Huberfield (Reg. No. 26,665); Todd M.C. Li (Reg. No. 45,554); Anthony N. Magistrale (Reg. No. 35,595); Margaret A. Pepper (Reg. No. 45,008); H. Daniel Schnurmann (Reg. No. 35,791); Eugene I. Shkurko (Reg. No. 36,678); Steven Soucar (Reg. No. 32,440); Tiffany L. Townsend (Reg. No. 43,199); Christopher A. Hughes, (Reg. No. 26,914); Edward A. Pennington, (Reg. No. 32,588); John E. Hoel, (Reg. No. 26,279); and Joseph C. Redmond, Jr., Reg. No. (18,753).

Address all telephone calls to: Robert Curcio of DeLIO & PETERSON, LLC at (203) 787-0595.

Address all correspondence to: DeLIO & PETERSON, LLC, (Cust. No. 29505)

121 Whitney Avenue New Haven, CT 06510

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

(1)	Inventor:	Edmund D. Blackshear
	Signature:	
	_	Date
	Residence:	295 Cedar Hill Road, Wappingers Falls, New York 12590
	Citizenship:	USA
	Post Office Address:	SAME AS RESIDENCE

ibmf100407000dec

IBM Docket No.:

FIS920030315US1

States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

295 Cedar Hill Road, Wappingers Falls, New York 12590

(1) Inventor:

Edmund D_Blackshear

Signature:

Residence:

· ,

Citizenship: USA

Post Office

Address: SAME AS RESIDENCE

ibmf100407000dec

No.:

FIS920030315US1

Patents of Invention or Design - Thailand

DEED OF ASSIGNMENT

Date:

Title of Invention/Design: OVERLAP STACKING OF CENTER BUS BONDED MEMORY CHIPS FOR DOUBLE DENSITY AND METHOD OF MANUFACTURING THE SAME

Assignor(s):

(1) Edmund D. Blackshear

Assignee:

International Business Machines Corporation

Armonk, N.Y. 10504, USA

The Assignor(s) declare that I/we are the inventor(s) of the above-mentioned invention/design and for due consideration hereby assign the right to apply for and obtain patents therefor in Thailand to the Assignee and the Assignee hereby accepts such assignment.

In witness whereof, the Assignor(s) and Assignee have signed their names below, effective on the date above.

ASSIGNORS

Edmund D. Blackshear